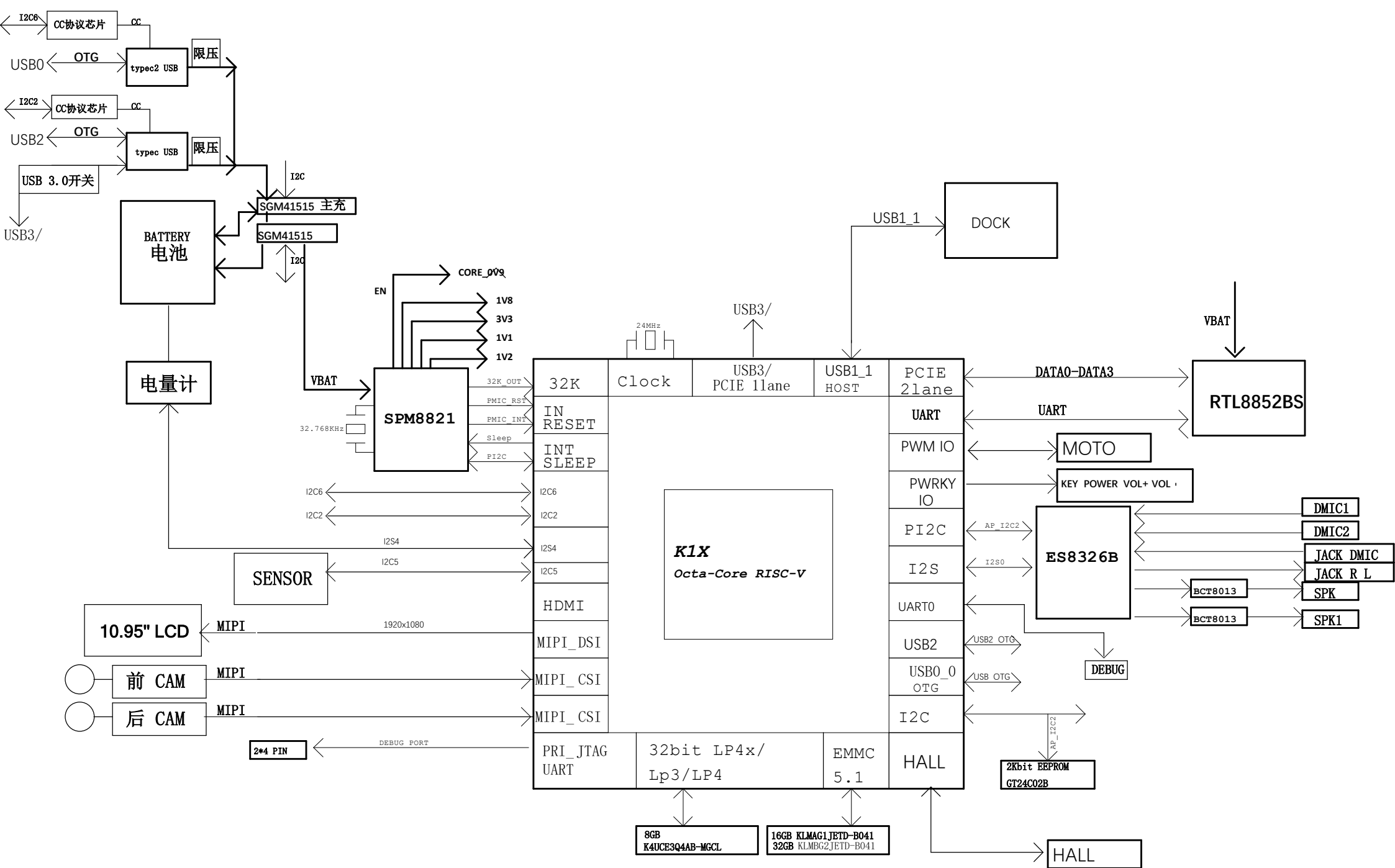
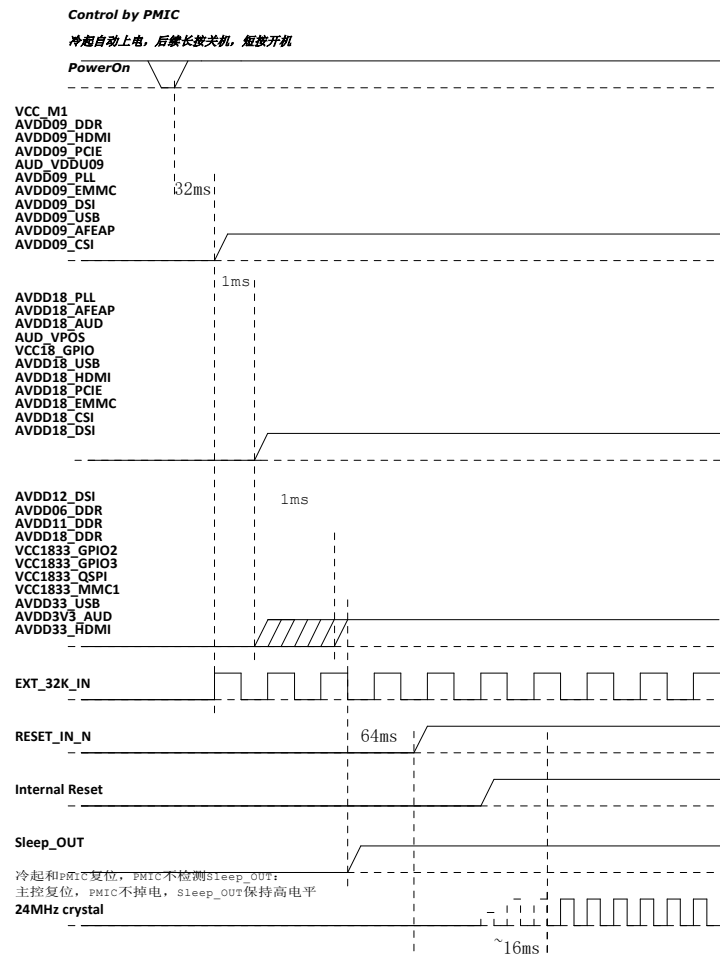




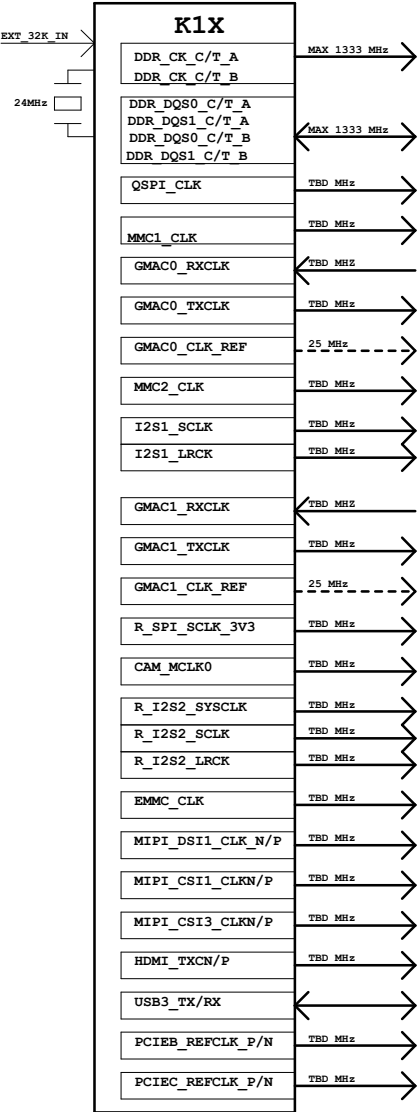
POWER SEQUENCE



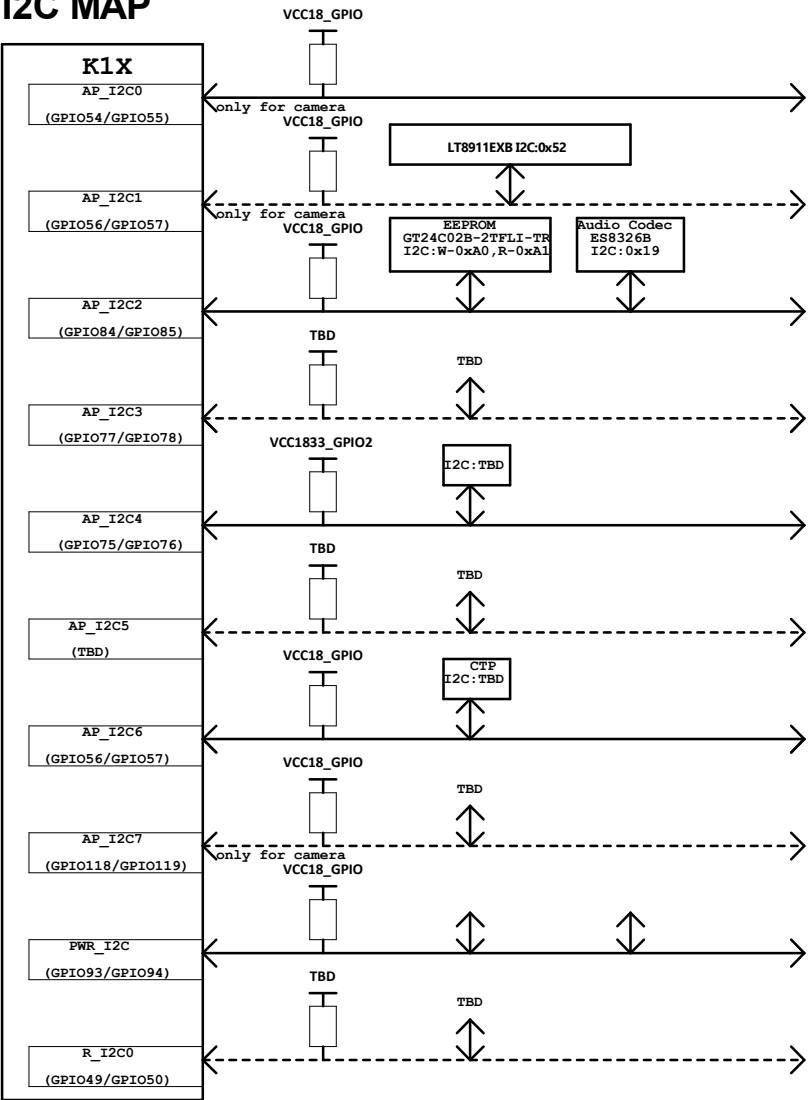
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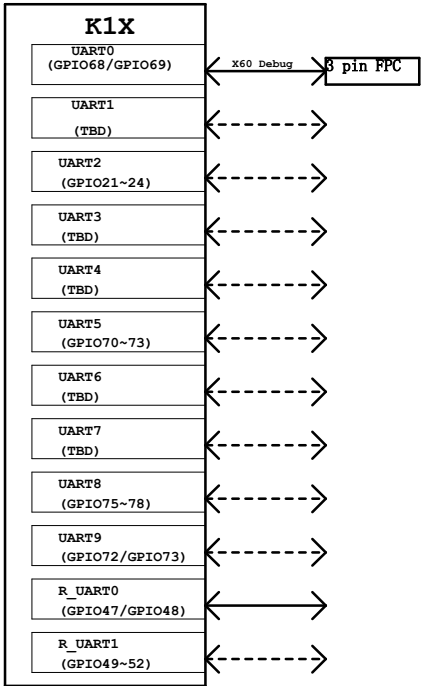
CLOCK MAP



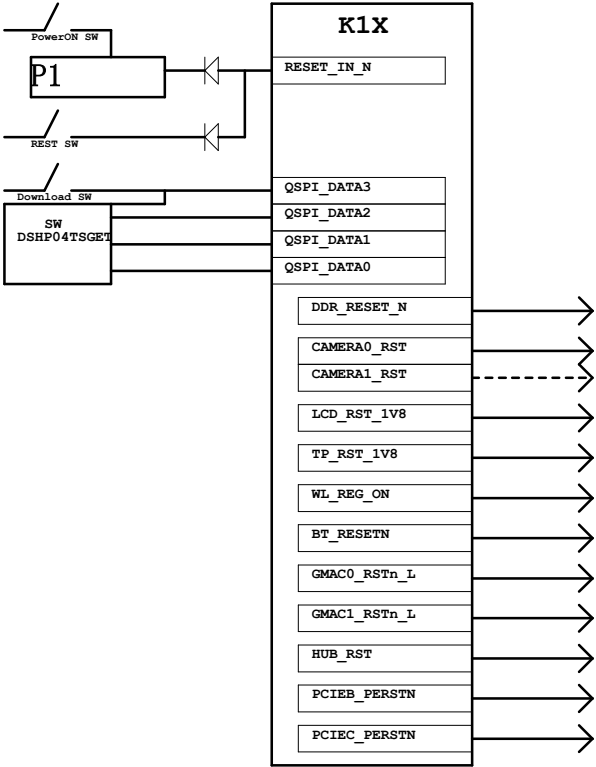
I2C MAP



UART MAP



SW&RESET MAP



GPIO ASSIGNMENT

PIN	Define	CFG	Function
GPIO0		1	
GPIO1		1	
GPIO2		1	
GPIO3		1	
GPIO4		1	
GPIO5		1	
GPIO6		1	
GPIO7		1	
GPIO8		1	
GPIO9		1	
GPIO10		1	
GPIO11		1	
GPIO12		1	
GPIO13		1	
GPIO14		1	
GPIO15		1	
GPIO16		1	
GPIO17		1	
GPIO18		1	
GPIO19		1	
GPIO20		1	
GPIO21		1	
GPIO22		1	
GPIO23		1	
GPIO24		1	WiFi/BT
GPIO25	BT_PCM_CLK	1	
GPIO26	BT_PCM_SYNC	1	
GPIO27	BT_PCM_DIN	1	
GPIO28	BT_PCM_DOUT	1	Ethernet -GMAC1
GPIO29		1	
GPIO30		1	
GPIO31		1	
GPIO32		1	
GPIO33		1	
GPIO34		1	
GPIO35		1	
GPIO36		1	
GPIO37		1	
GPIO38		1	
GPIO39		1	
GPIO40		1	
GPIO41		1	
GPIO42		1	
GPIO43		1	
GPIO44	LCD_BL_PWM_1V8	4	LCD/CTP
GPIO45		1	GMAC0
GPIO46		1	GMAC1

PIN	Define	CFG	Function
GPIO110		0	
GPIO115		0	
GPIO116		0	WiFi/BT
GPIO117	PCIEC_CLKREQN	4	PCIEC
GPIO118	I2S0_SCLK	3	Audio Codec
GPIO119	I2S0_LRCK	3	
GPIO120	I2S0_OUT	3	
GPIO121	I2S0_IN	3	
GPIO122	I2S0_SYSCLK	3	
GPIO123	HUB_PWREN	0	USB3 HUB
GPIO124		0	
GPIO125	VBUS_ON0	1	USB2
GPIO126	CODEC_IRQ	0	Audio
GPIO127	PA_SHUTDOWN	0	Codec

PIN	Define	CFG	Function
GPIO53		1	
GPIO54		1	
GPIO55	BAT_THERM	1	BATTERY
GPIO56	AP_I2C6_SCL	5	LCD/CTP
GPIO57	AP_I2C6_SDA	5	
GPIO58	EDP_INTN	0	
GPIO111	BAT_INT	1	BATTERY
GPIO112	PCIEC_WAKEN	3	PCIEC
GPIO113		1	
GPIO114	EDP_RSTN	0	LCD/CTP
GPIO63	SINK1	0	TYPEC
GPIO64		0	
GPIO65	BT_WAKE_HOST	0	WiFi/BT
GPIO66	SINK2	0	TYPEC
GPIO67		0	
GPIO68	UART0_TXD	2	X60 Debug
GPIO69	UART0_RXD	2	

PIN	Define	CFG	Function
GPIO59	PCIEB_PERSTN	4	PCIEB
GPIO60	PCIEB_WAKEN	4	
GPIO61	PCIEB_CLKREQN	4	
GPIO62	PCIEC_PERSTN	4	PCIEC
GPIO70	GPIO70	1	2X4 pin
GPIO71	GPIO71	1	
GPIO72	GPIO72	1	
GPIO73	GPIO73	1	
GPIO74	HALL_N	0	HALL

红色字体GPIO表示默认、持续上拉，等效上拉电阻约60K。需要软件修改才能解除默认上拉状态

PIN	Define	CFG	Function
GPIO93	PI2C_SCL	0	PMIC
GPIO94	PI2C_SDA	0	
GPIO95	SLEEP_OUT	0	
GPIO96		1	
GPIO97		1	
GPIO81	AP_I2C5_SCL	0	EC
GPIO82	AP_I2C5_SDA	0	
GPIO83	LCD_PWR_EN_1V8	0	LCD/CTP
GPIO84	AP_I2C2_SCL	4	EEPROM
GPIO85	AP_I2C2_SDA	4	Audio
GPIO86	HDMI_SCL	1	HDMI_OUT
GPIO87	HDMI_SDA	1	
GPIO88	HDMI_CEC	1	
GPIO89	HDMI_HPD	1	
GPIO90	GPIO90	0	SPI
GPIO91	GPIO91	0	WIFI
GPIO92	EDP_STANDBY	0	LCD/CTP

PIN	Define	CFG	Function
GPIO98	QSPI_DATA3	0	SPI FLASH
GPIO99	QSPI_DATA2	0	
GPIO100	QSPI_DATA1	0	
GPIO101	QSPI_DATA0	0	
GPIO102	QSPI_CLK	0	
GPIO103	QSPI_CS1	0	

PIN	Define	CFG	Function
GPIO104	MMC1_DATA3	0	TF CARD
GPIO105	MMC1_DATA2	0	
GPIO106	MMC1_DATA1	0	
GPIO107	MMC1_DATA0	0	
GPIO108	MMC1_CMD	0	
GPIO109	MMC1_CLK	0	

PIN	Define	CFG	Function
GPIO75	BL_ENABLE_3V3	2	LCD/CTP
GPIO76	WL_DIS_N	2	WiFi/BT
GPIO77	HOST_WAKE_BT	2	
GPIO78	PCIE1_BT_ON	2	
GPIO79	HUB_RST_3V3	2	USB3_HUB
GPIO80	SD_CD_3V3	1	TF CARD

PIN	Define	CFG	Function
GPIO47	R_UART0_TXD_3V3	1	2X4 pin
GPIO48	R_UART0_RXD_3V3	1	
GPIO49		0	
GPIO50		0	
GPIO51	AP_I2C4_SCL_3V3	4	TYPEC
GPIO52	AP_I2C4_SDA_3V3	4	BATTERY EC

Design Name

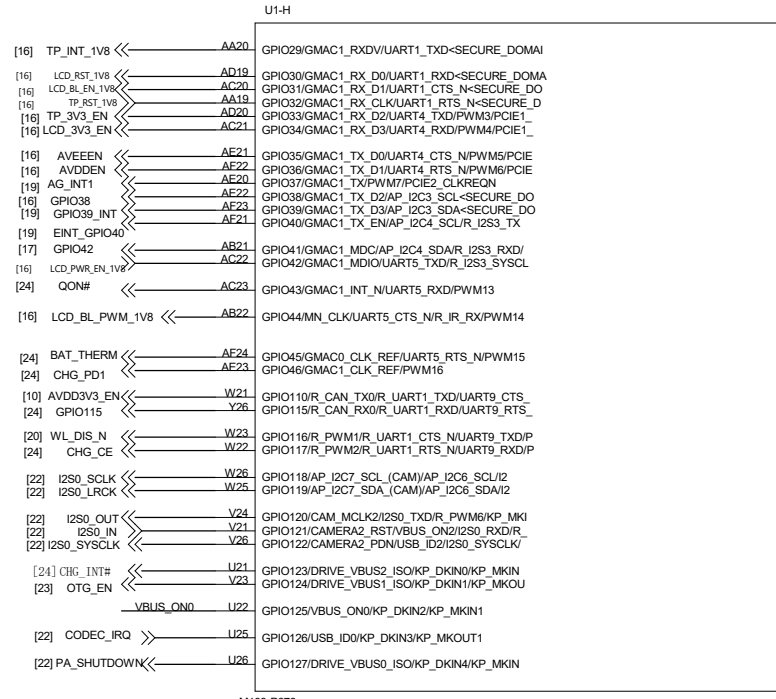
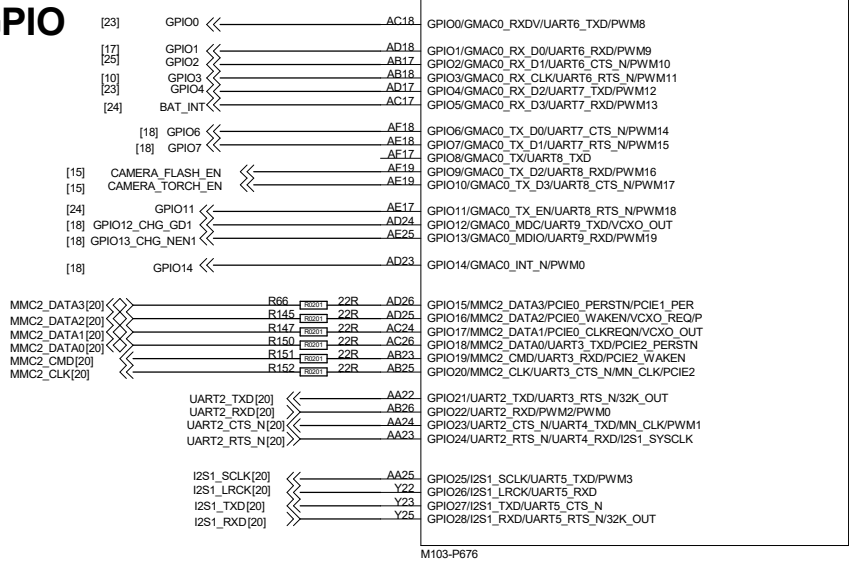
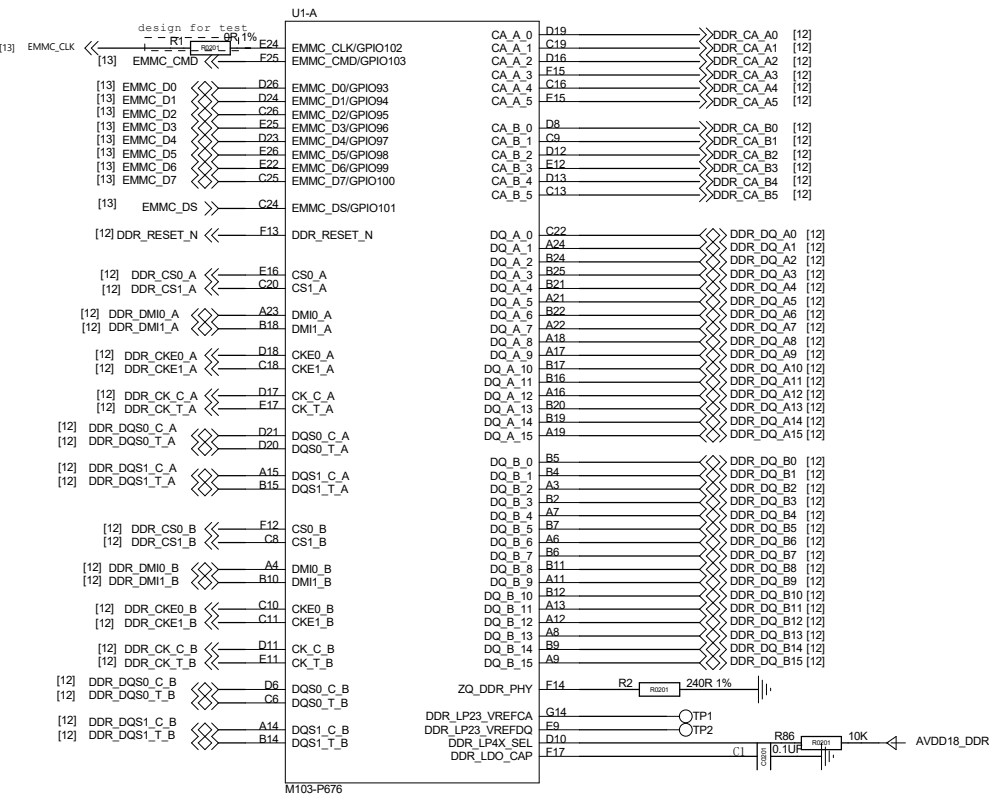
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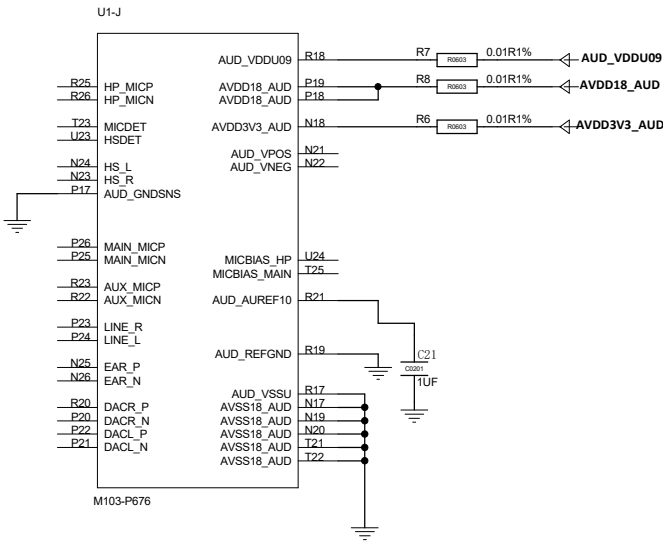
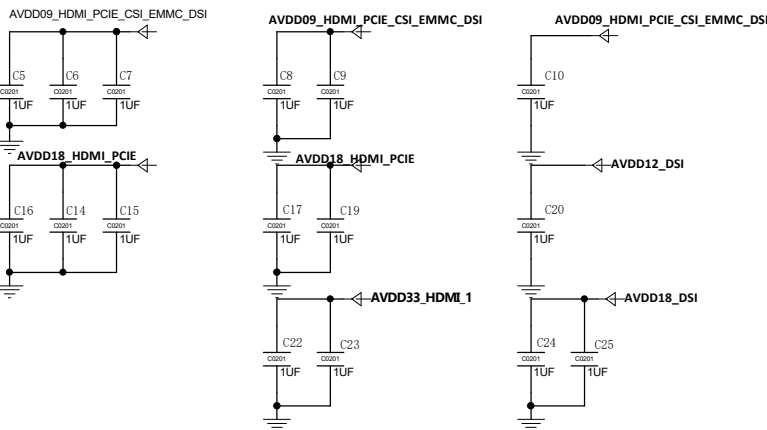
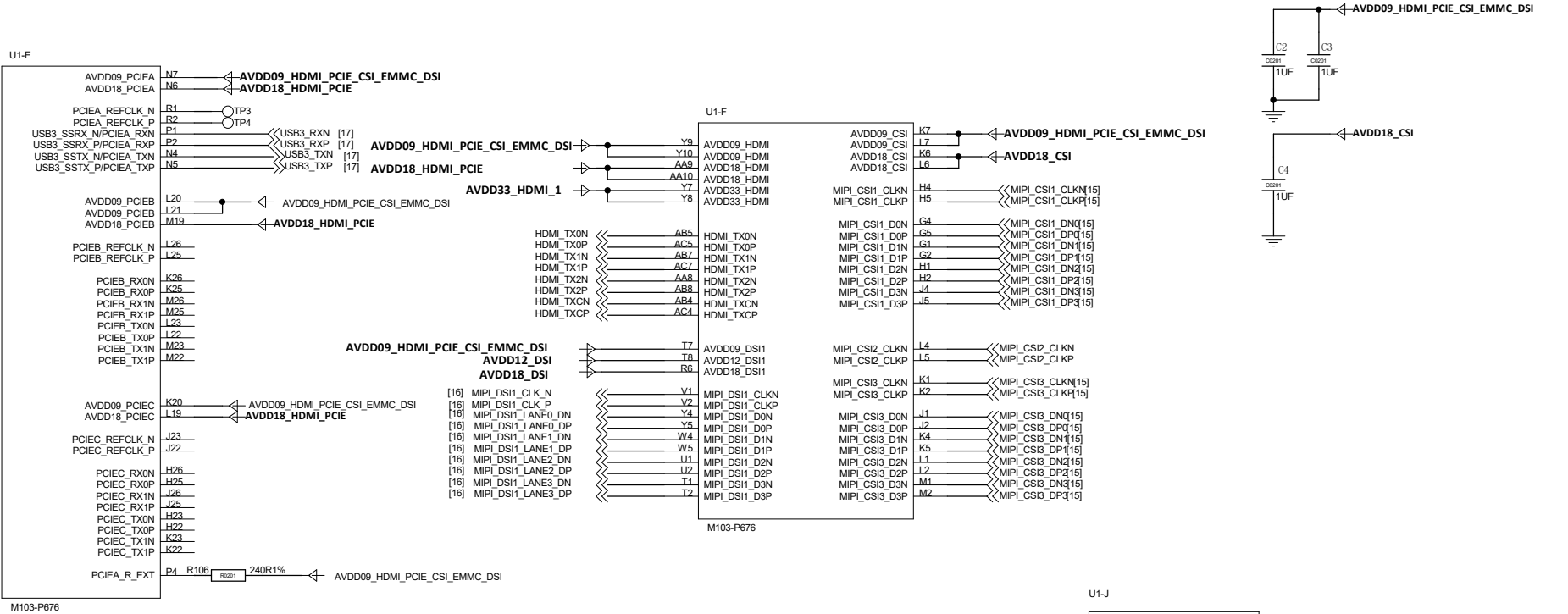
eMMC+DDR

GPIO



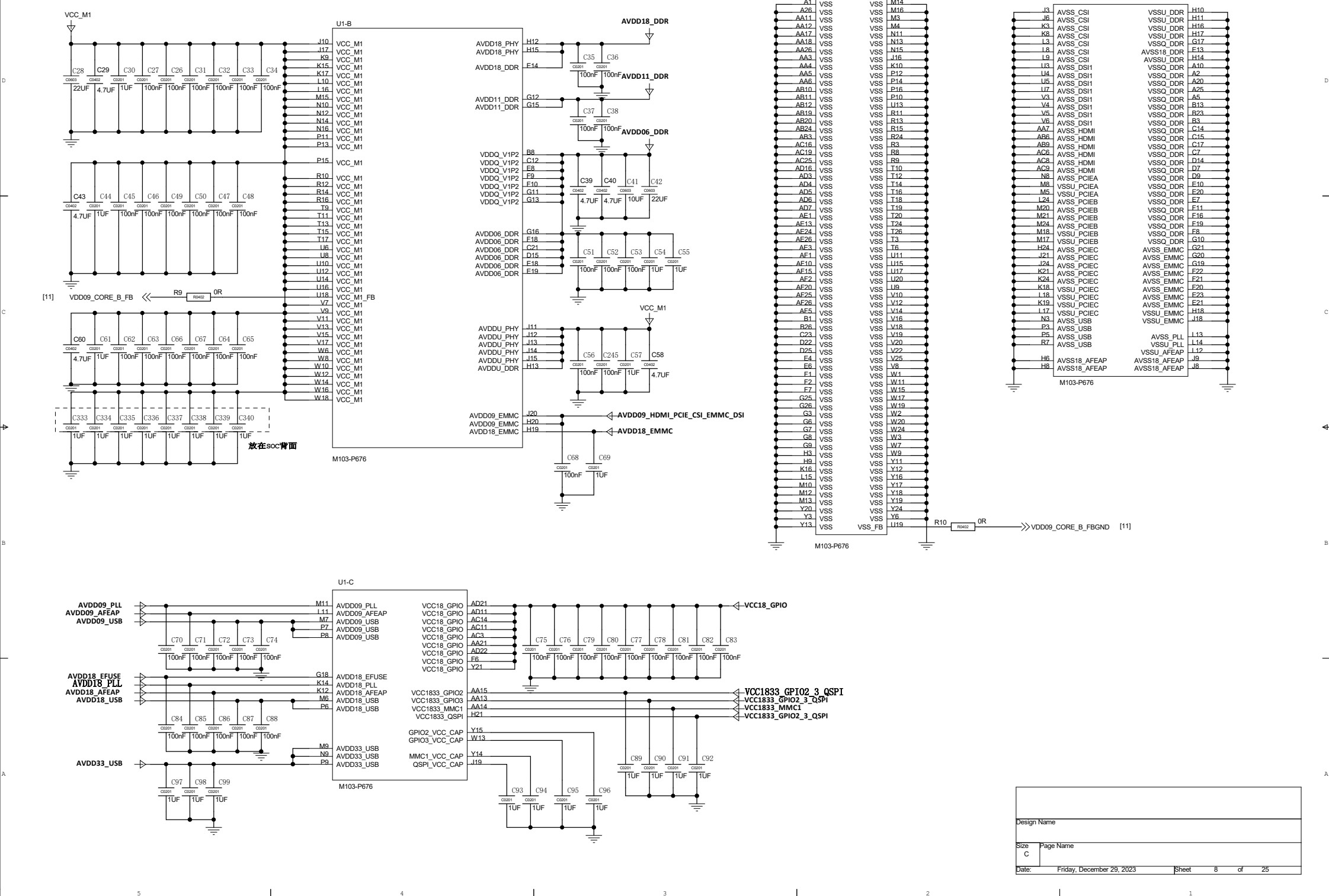
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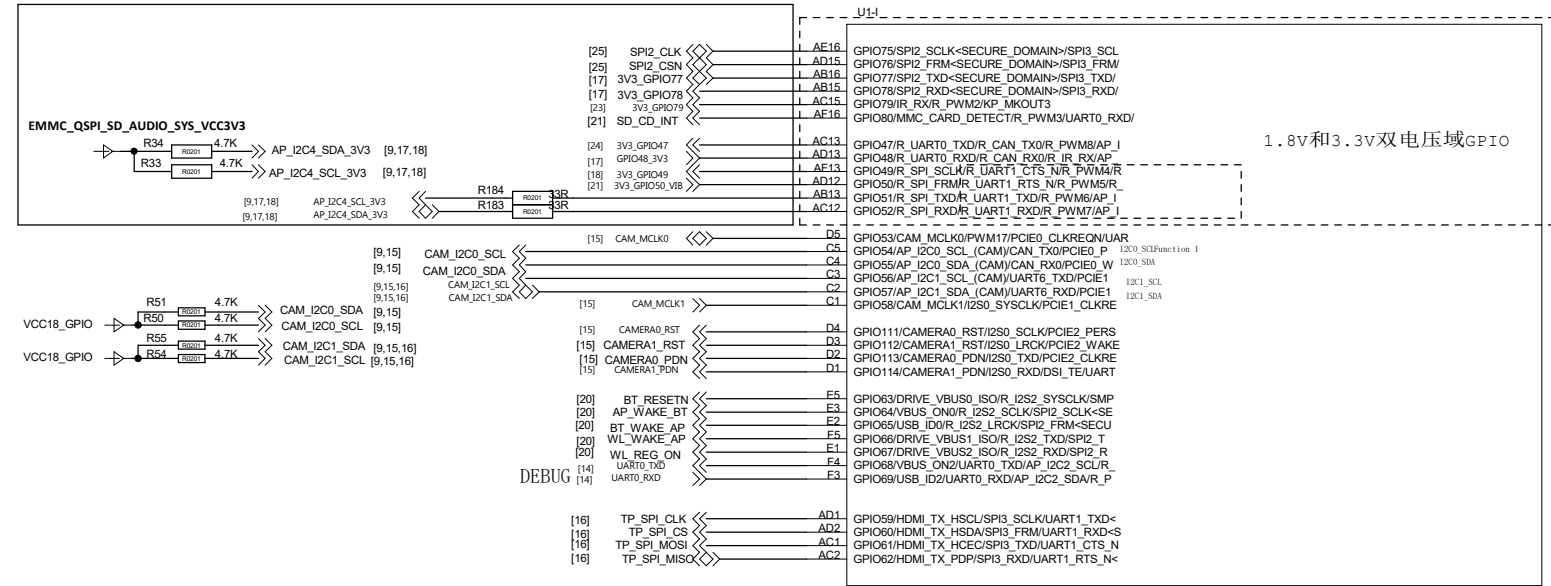
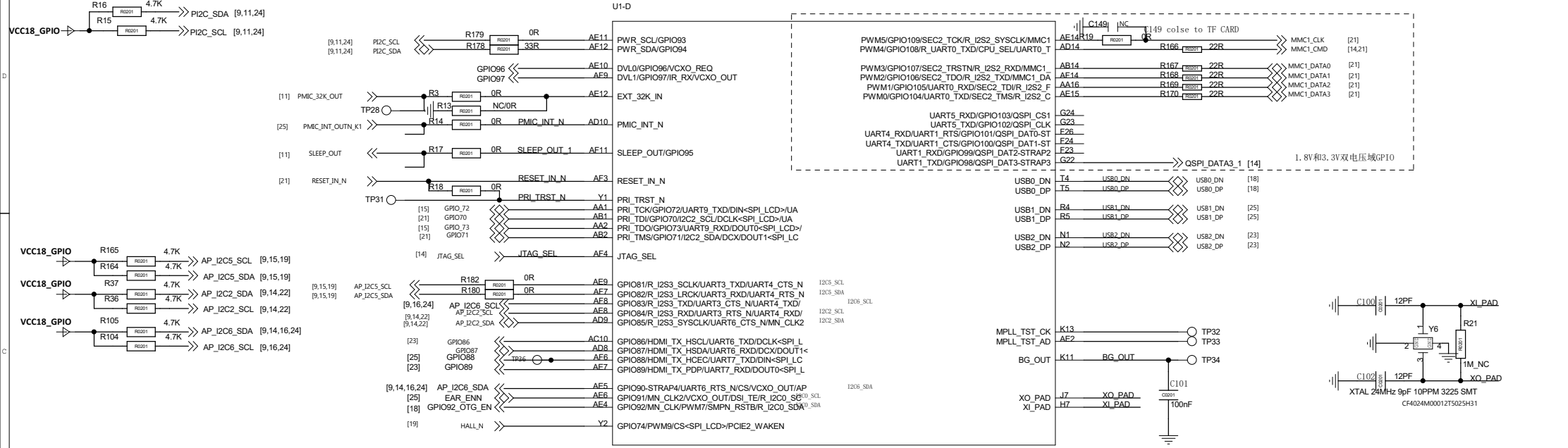


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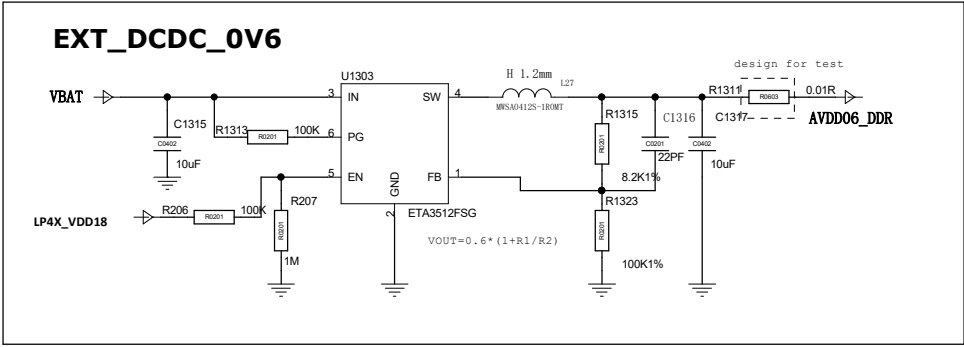
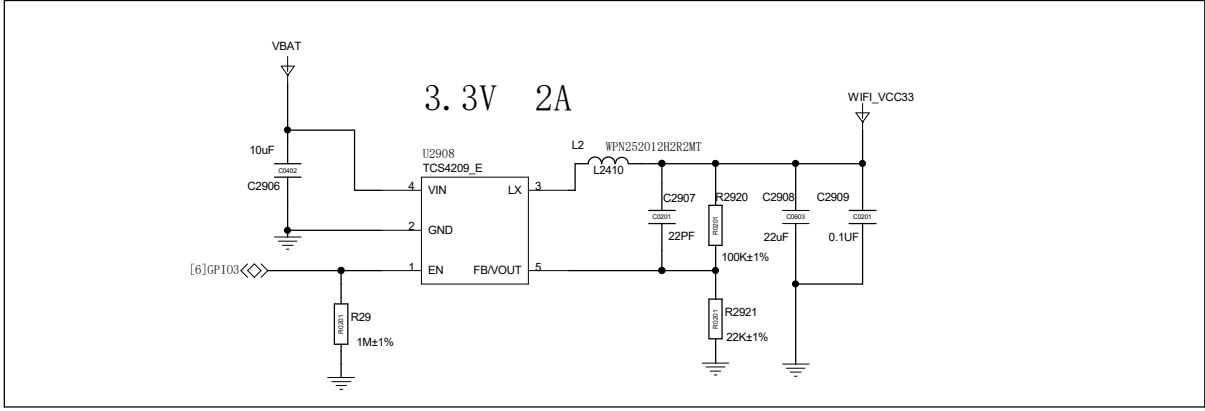
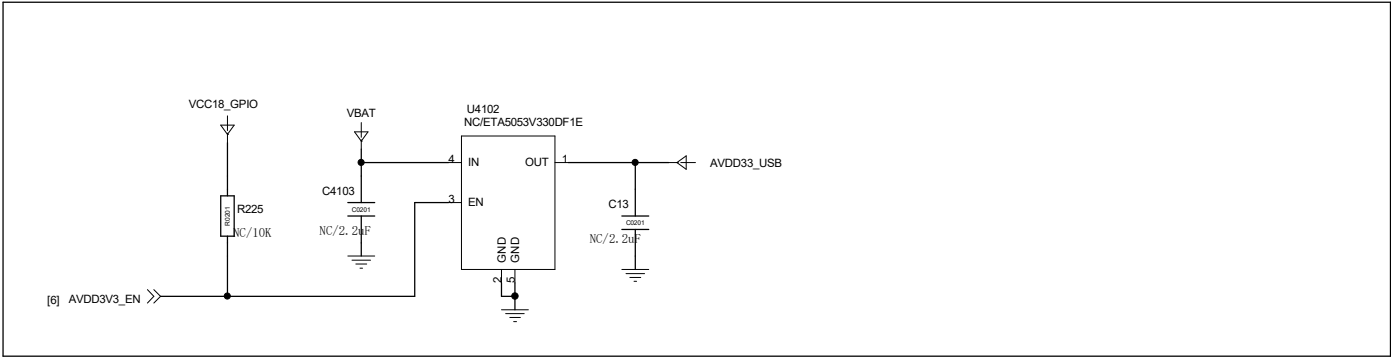
CPU-PWR



SYS-GPIO

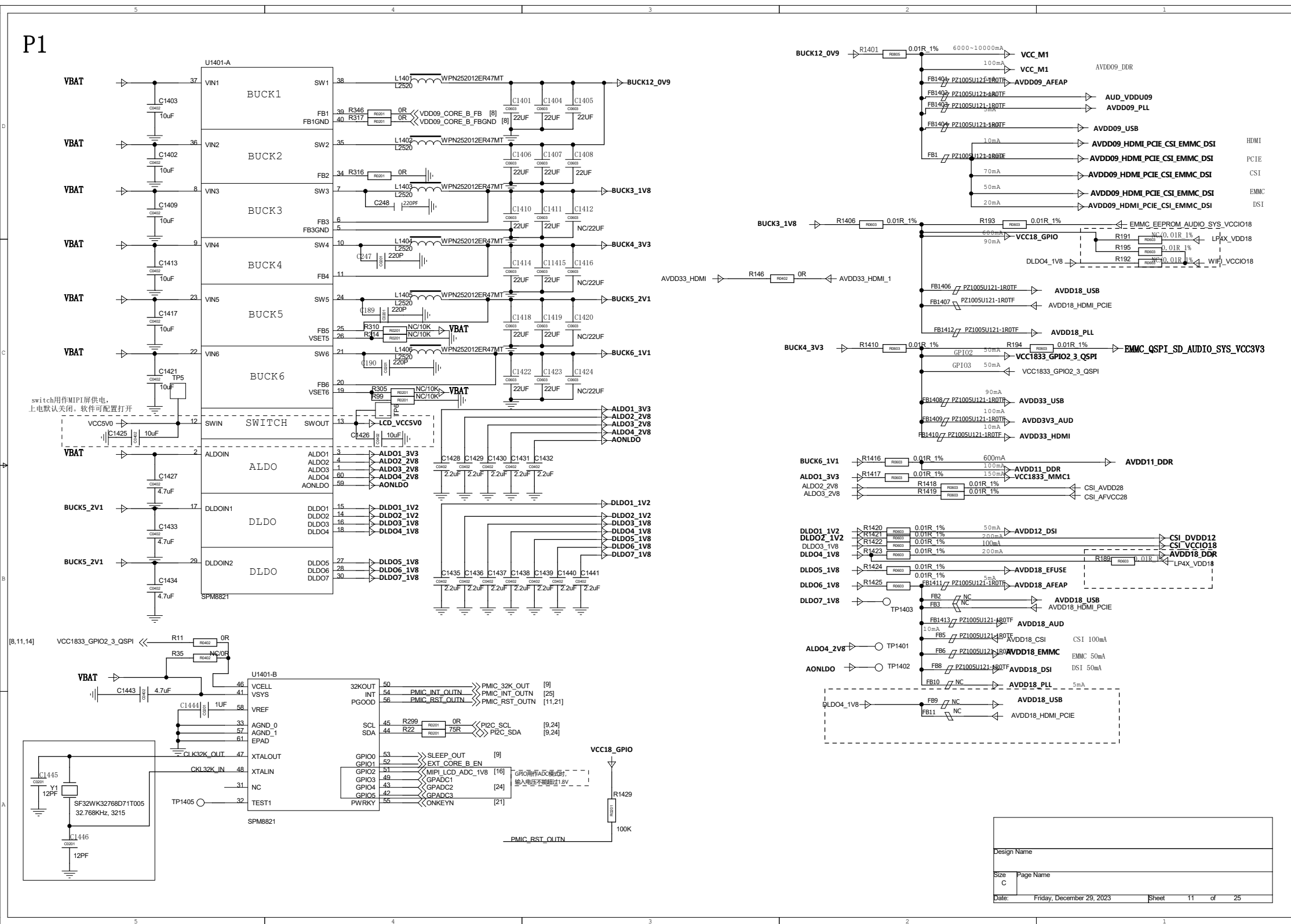


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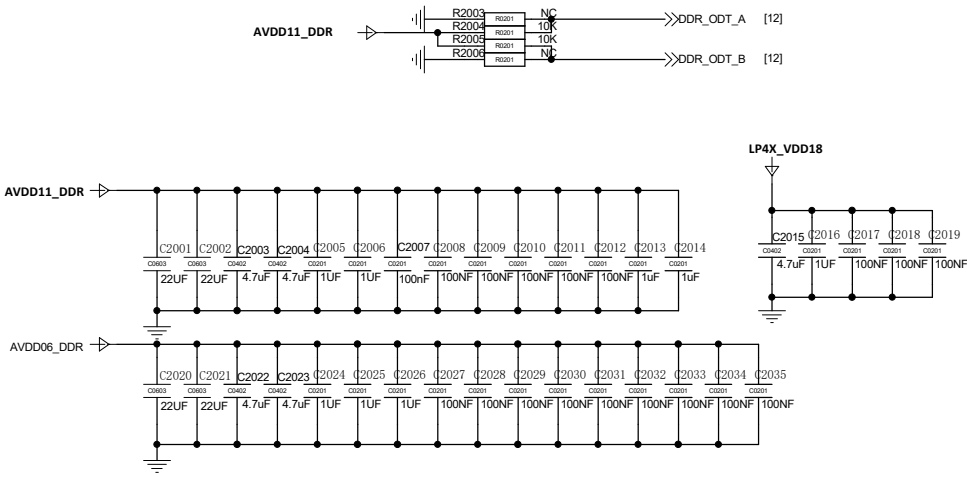
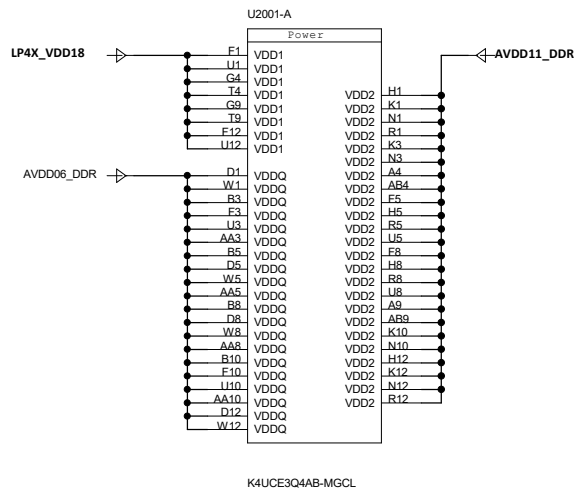
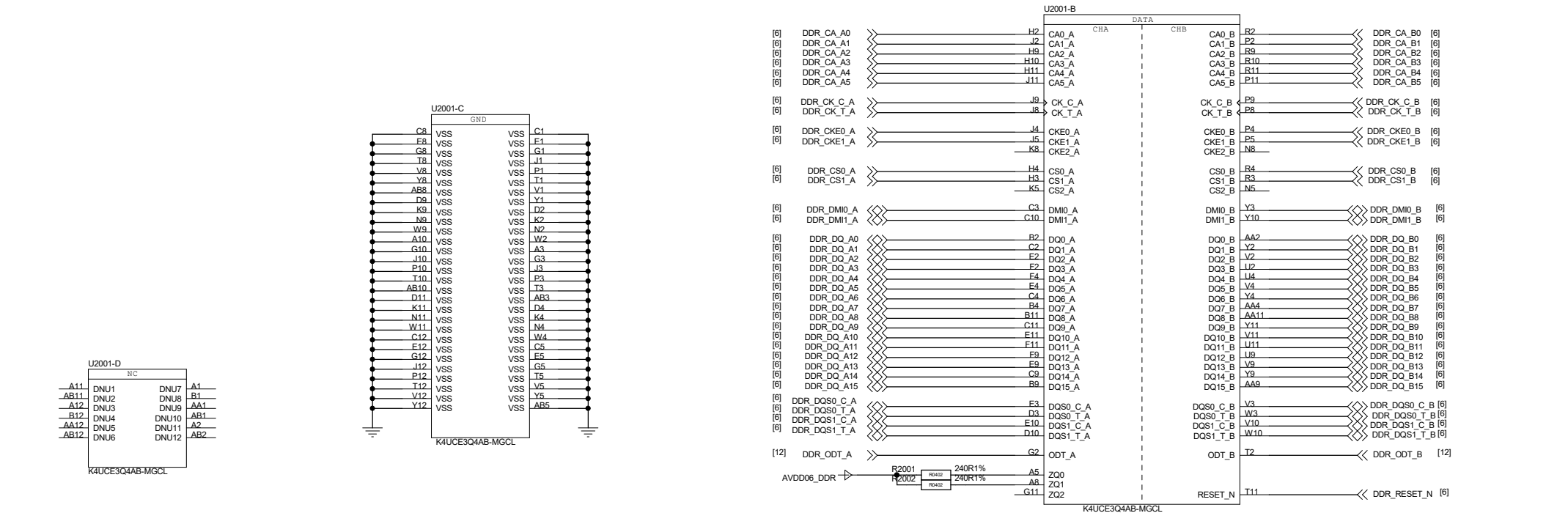
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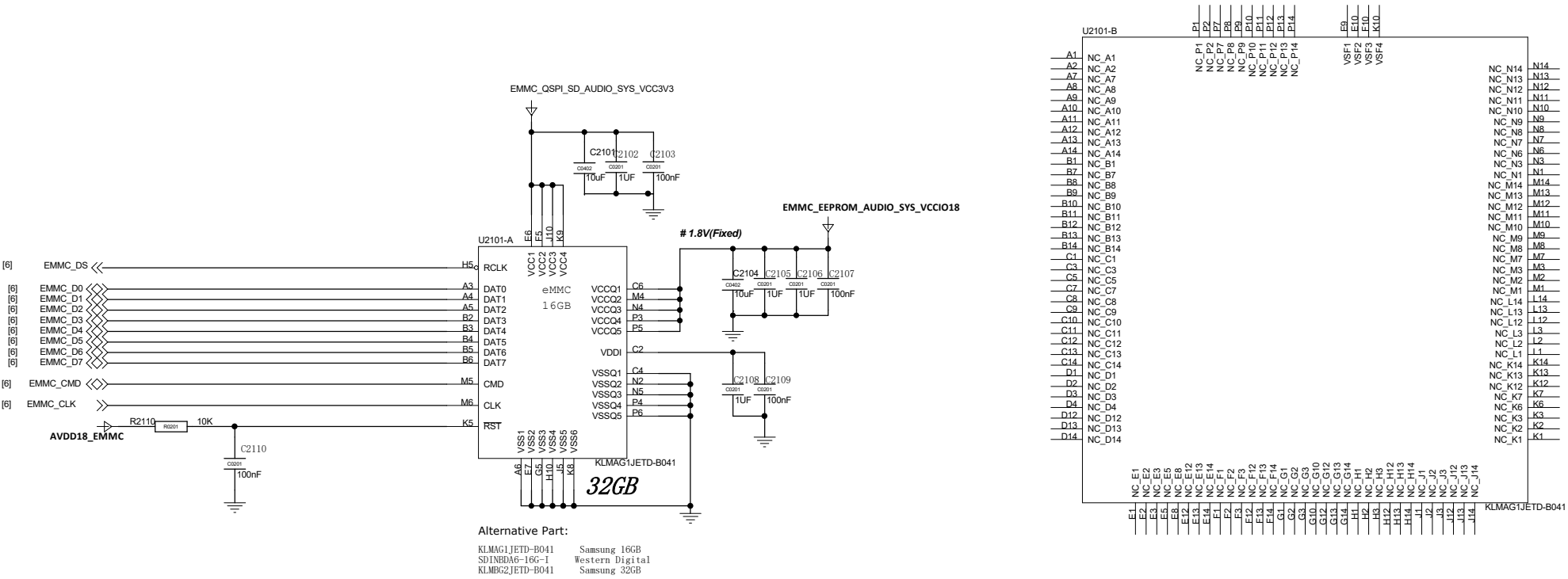
LPDDR4X

NOTE:CKE2/CS2/ZQ2 NC IN SUMSUNG



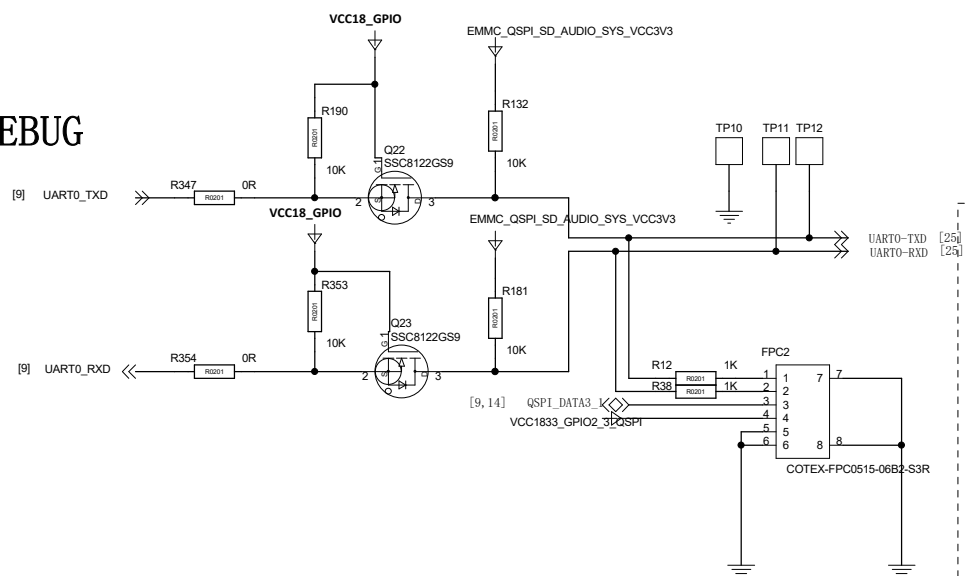
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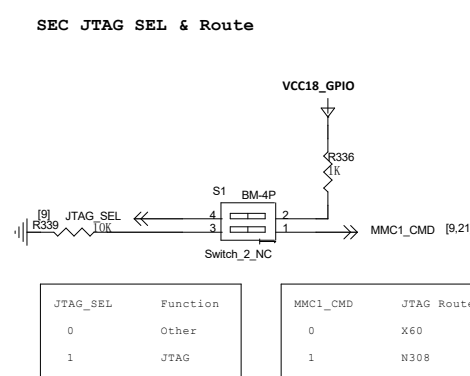


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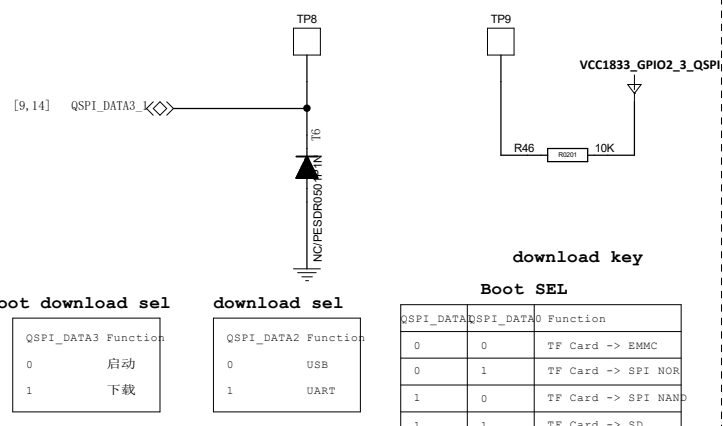
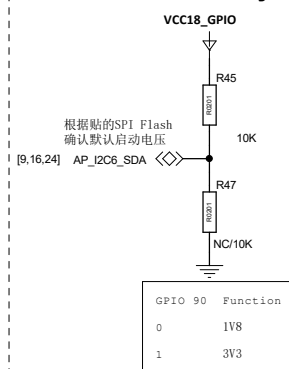
DEBUG



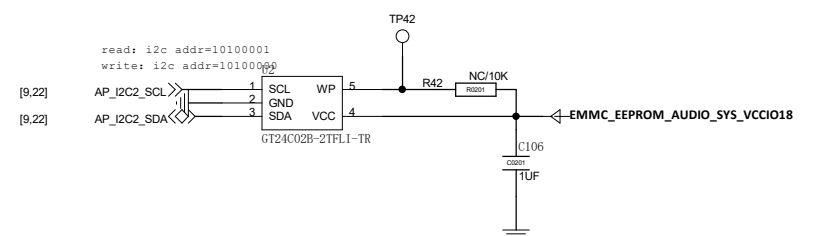
JTAG SEL



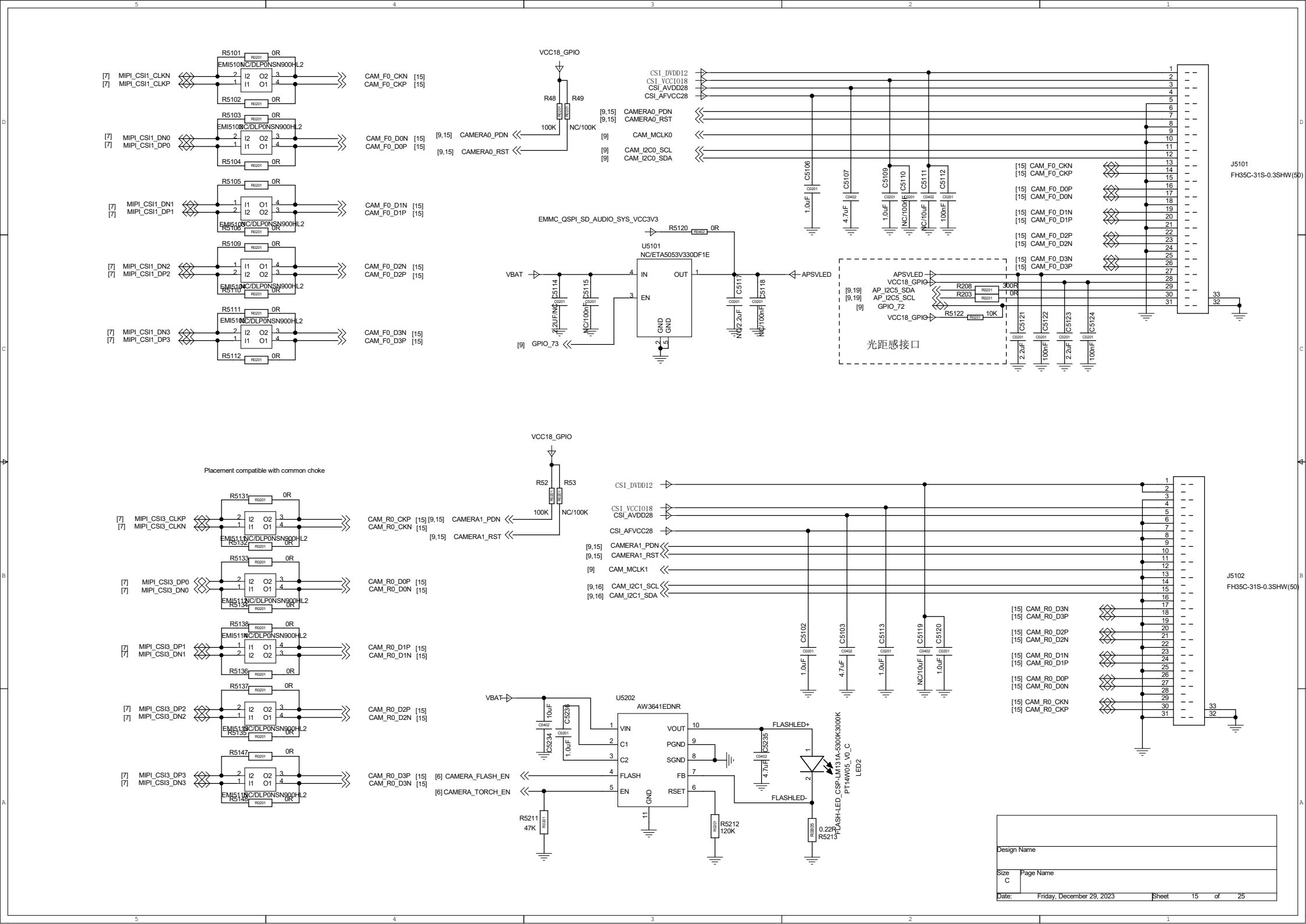
Flash Boot voltage sel

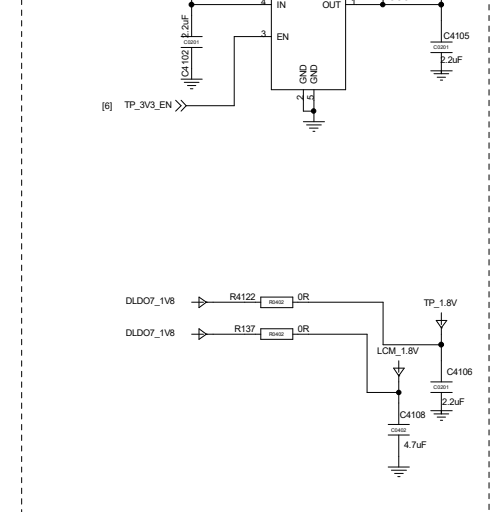
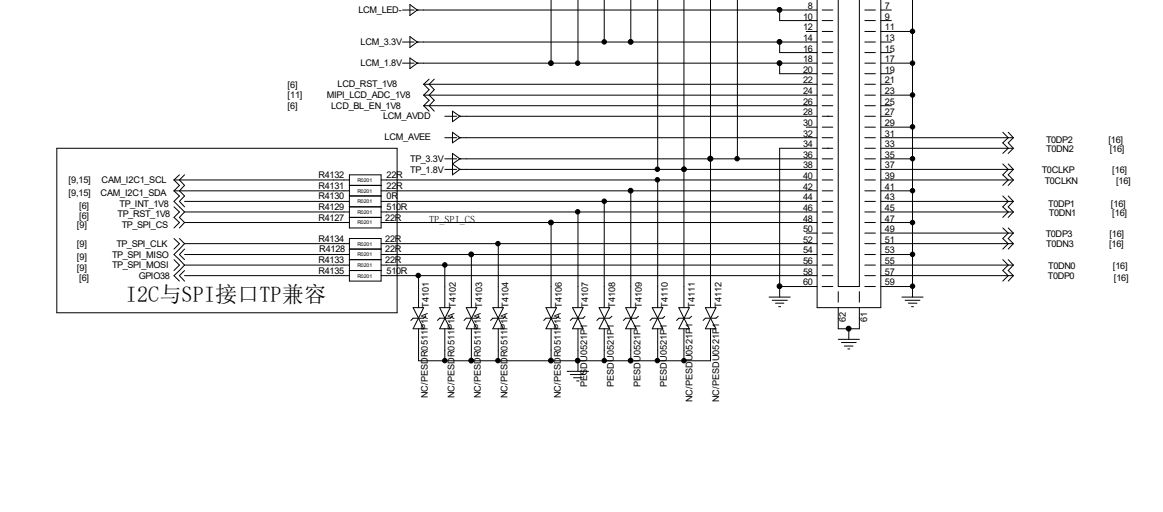
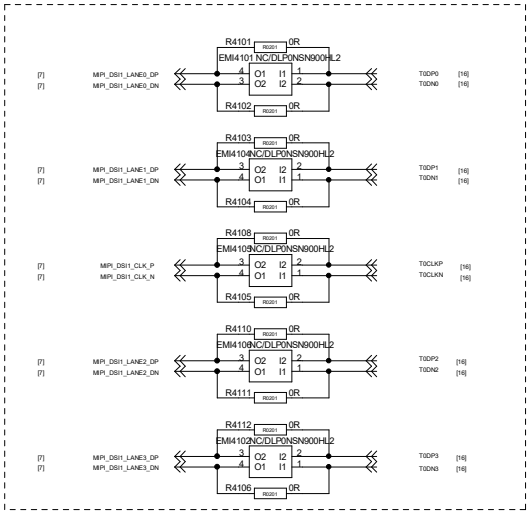
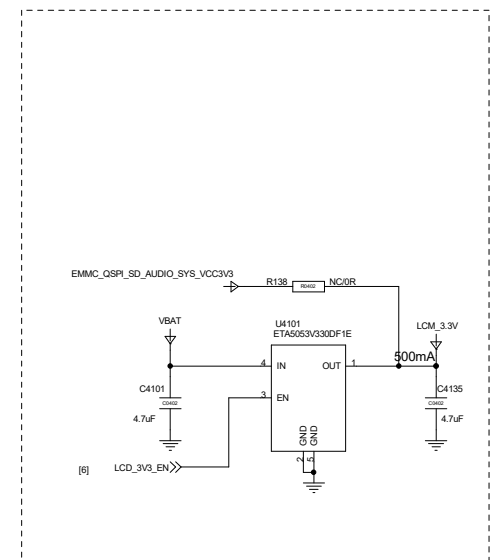
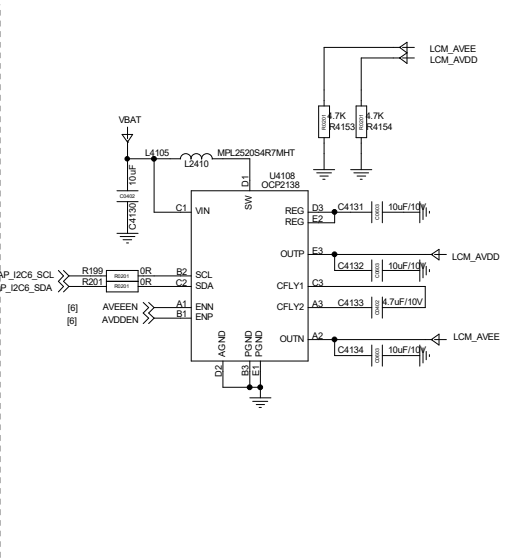
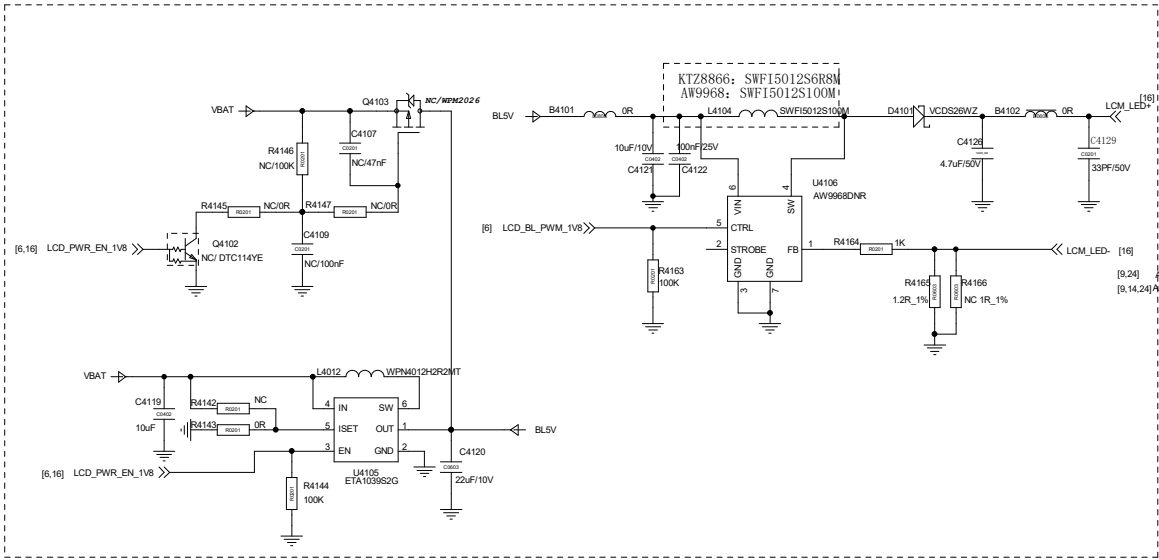


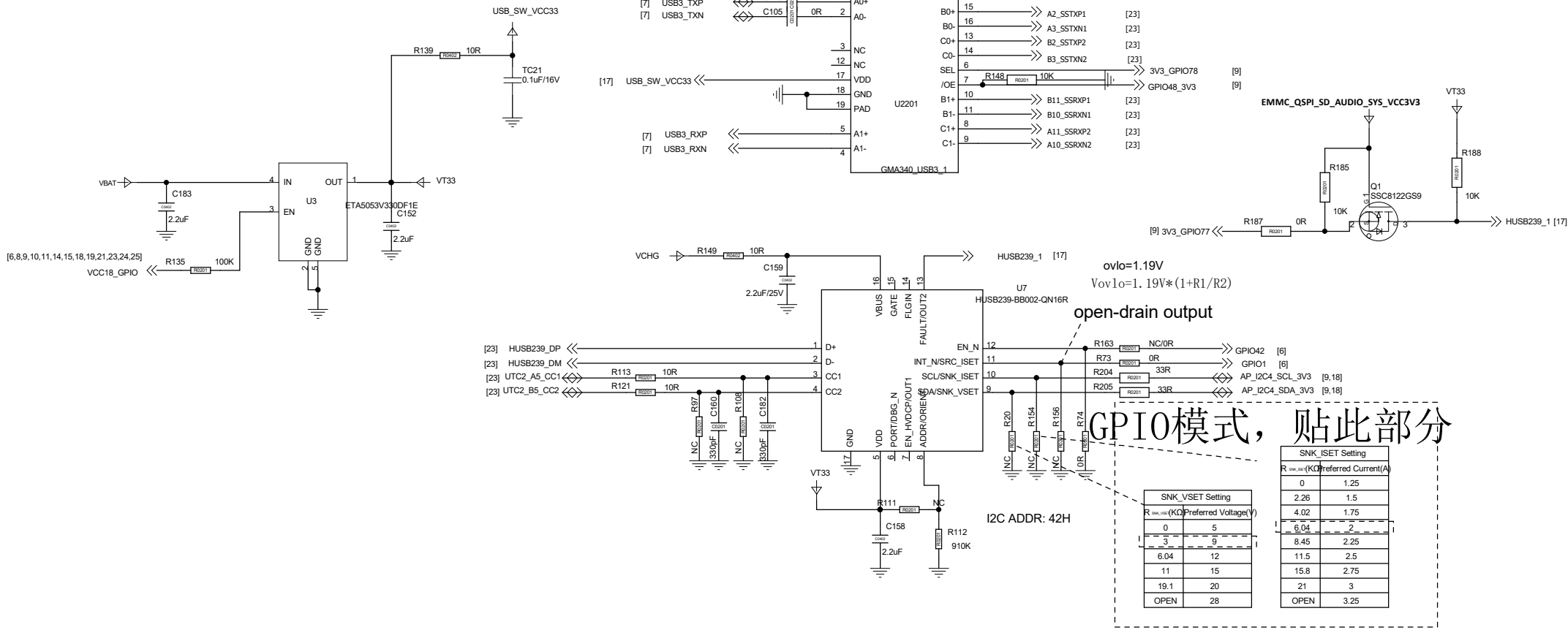
EEPROM

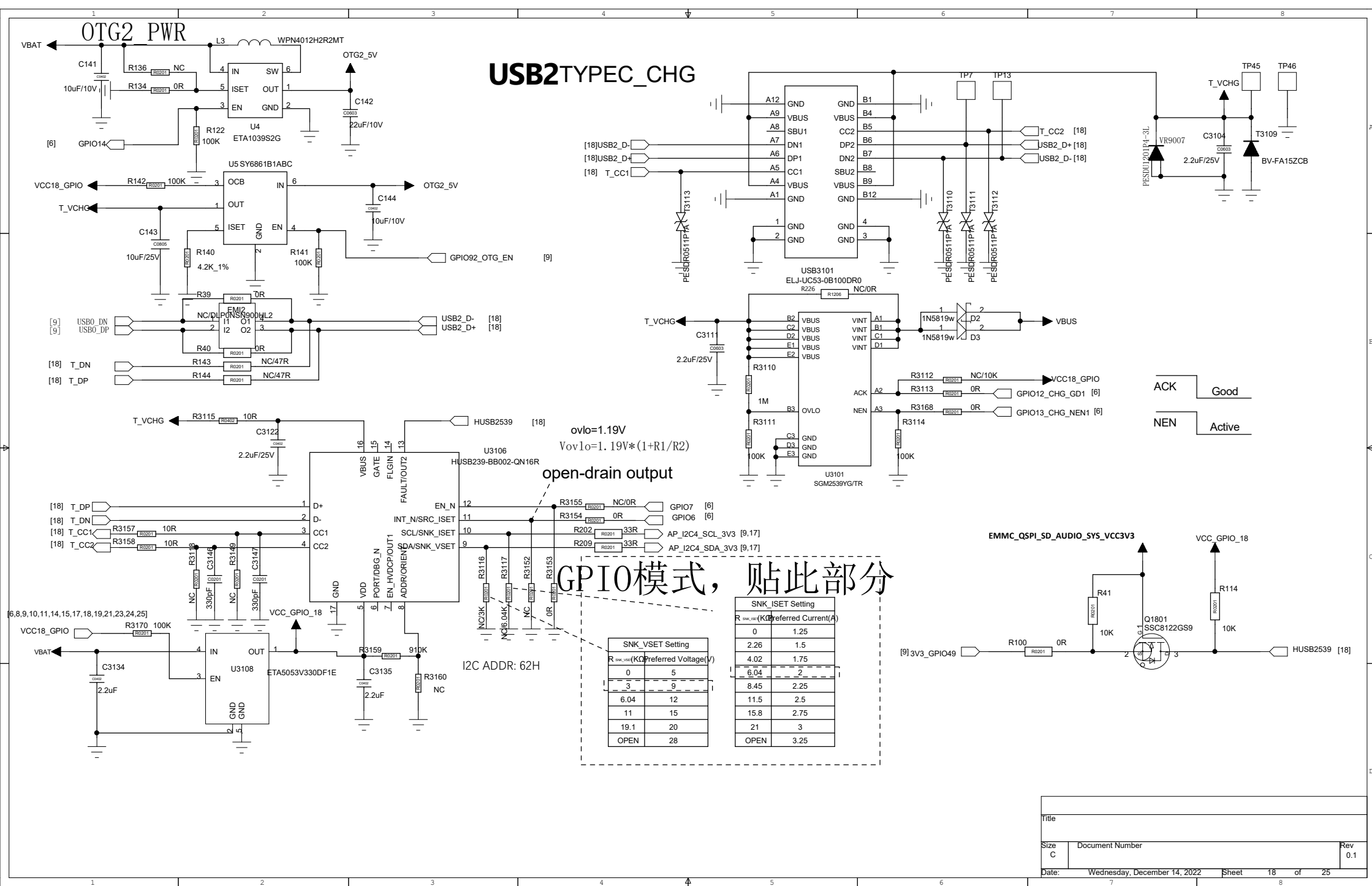


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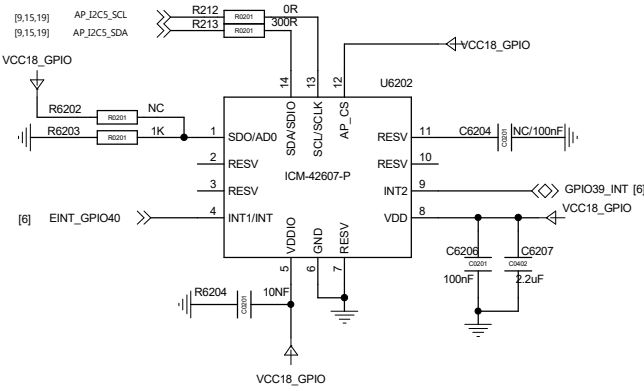
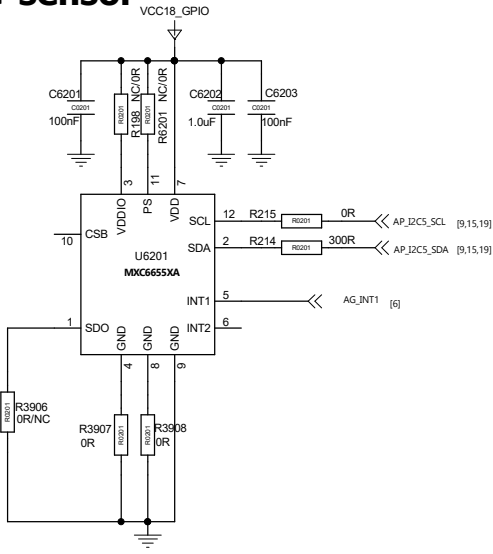




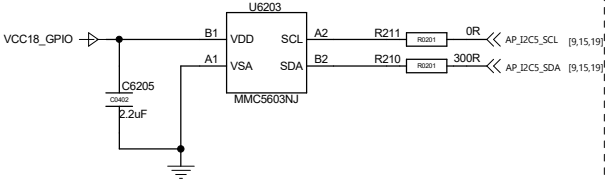




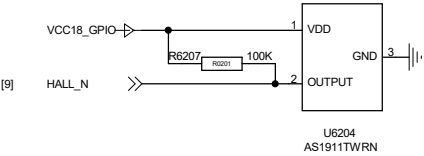
3axis G-sensor

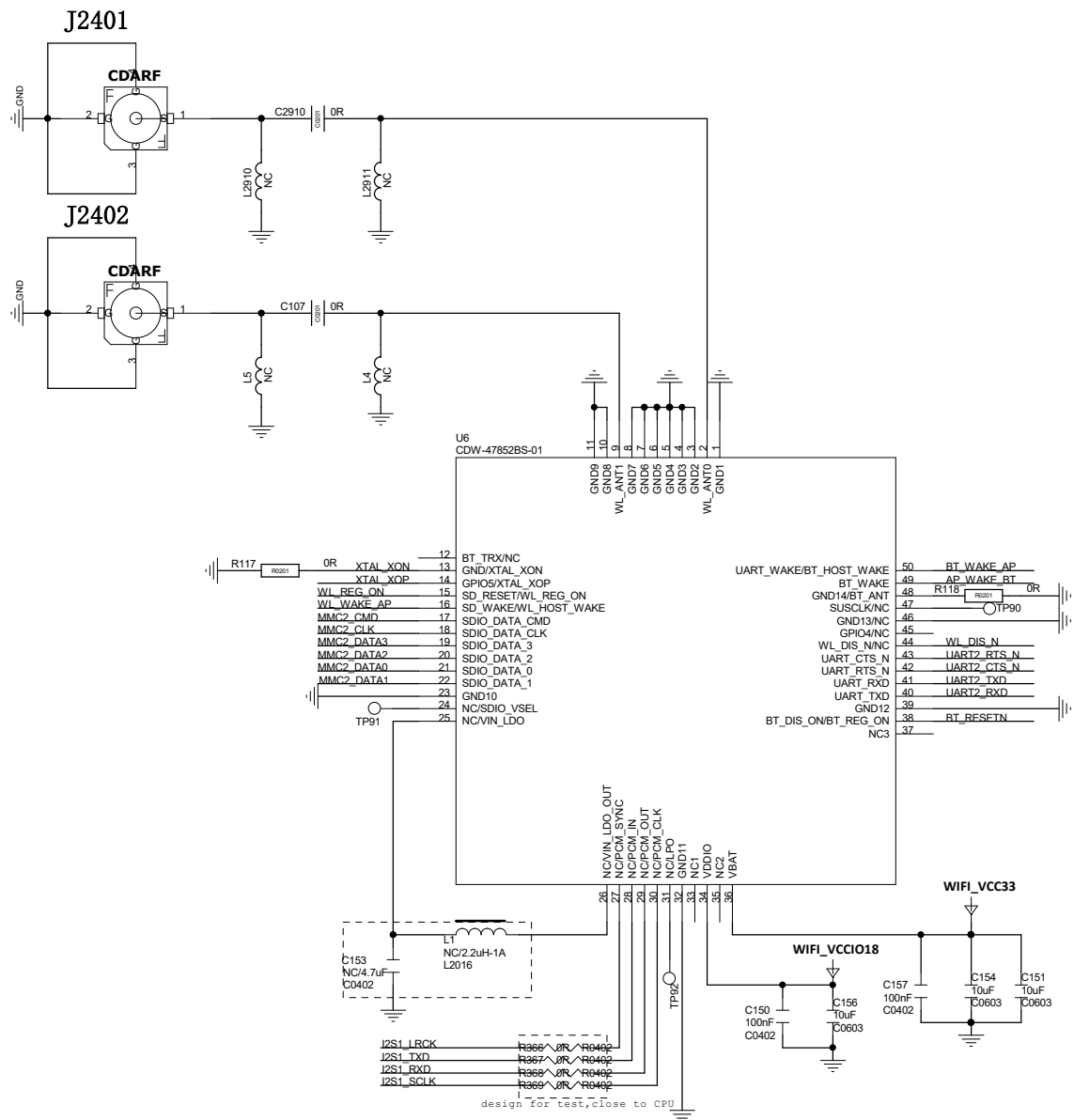
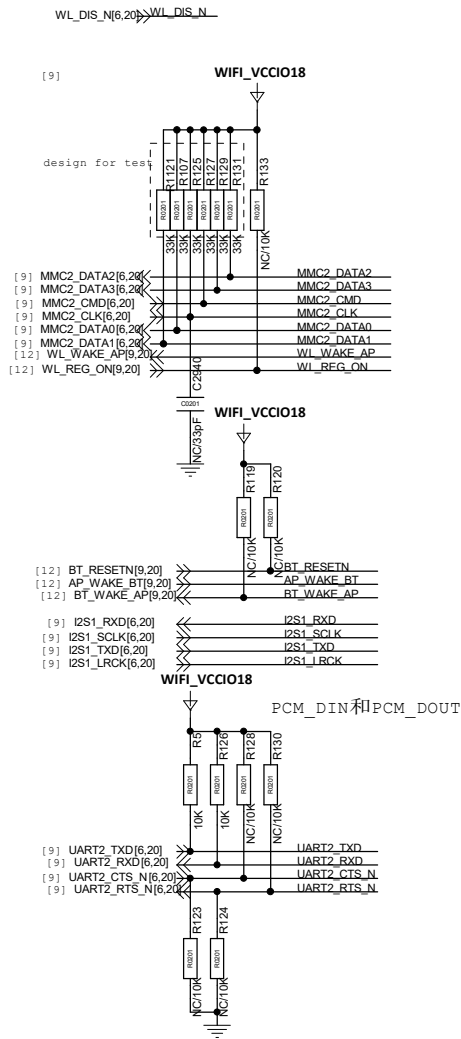


Magnetic Sensor



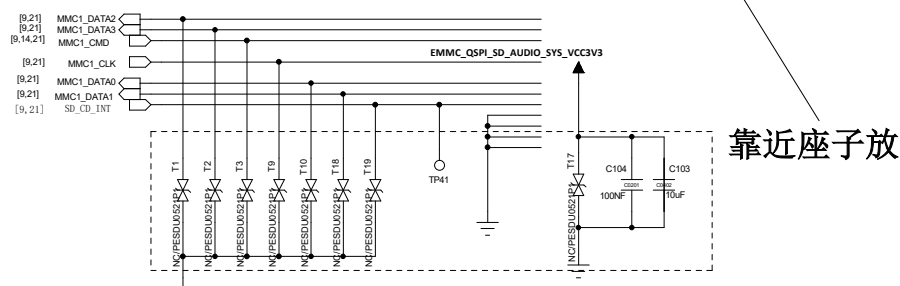
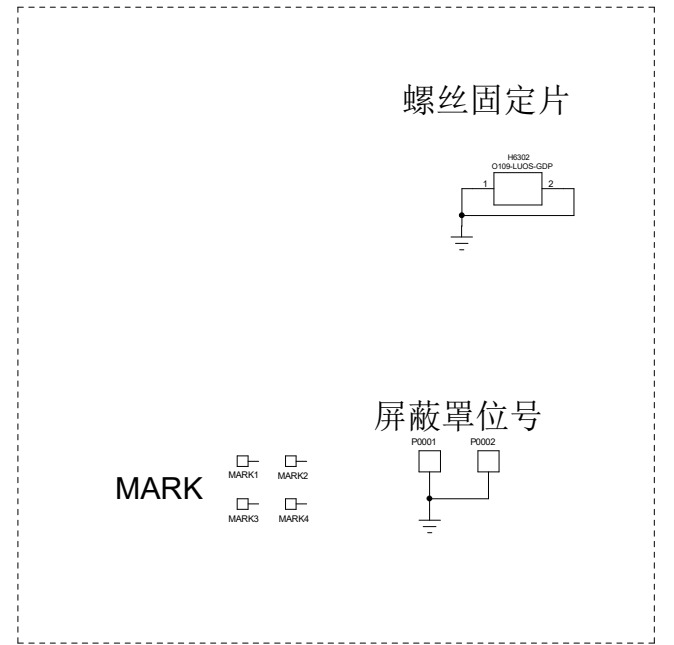
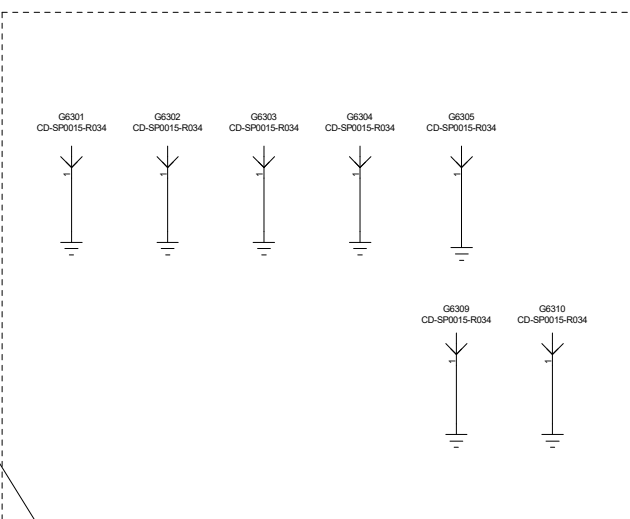
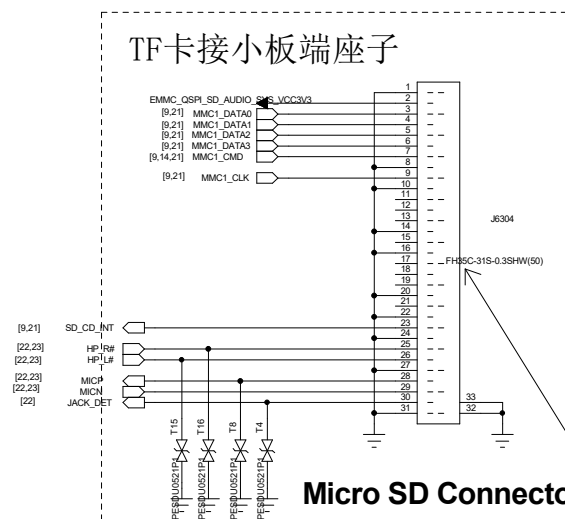
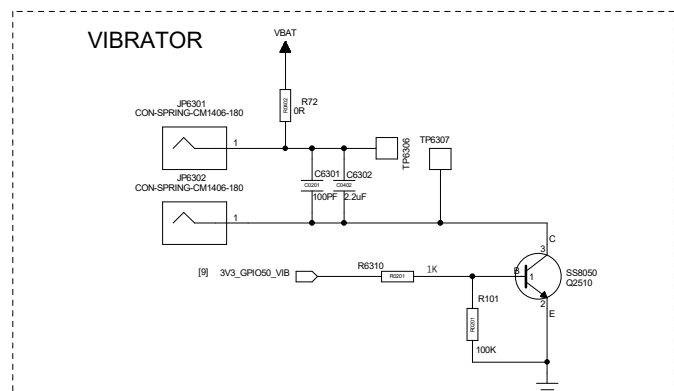
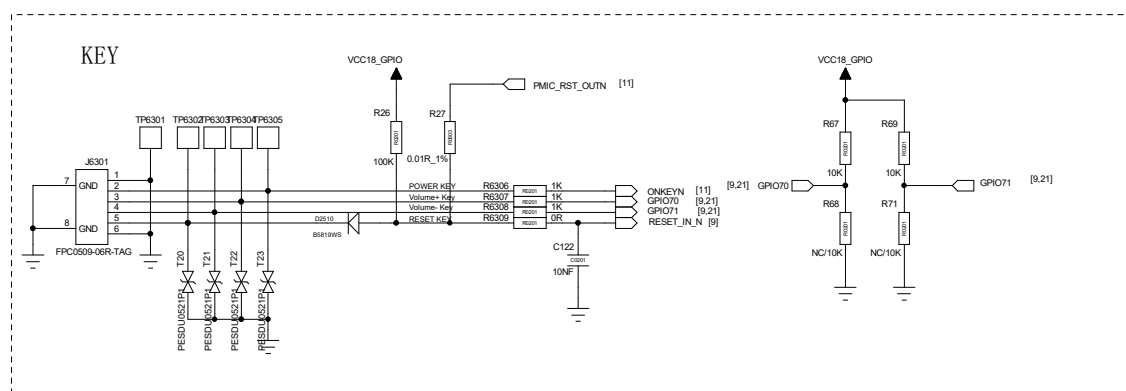
HALL





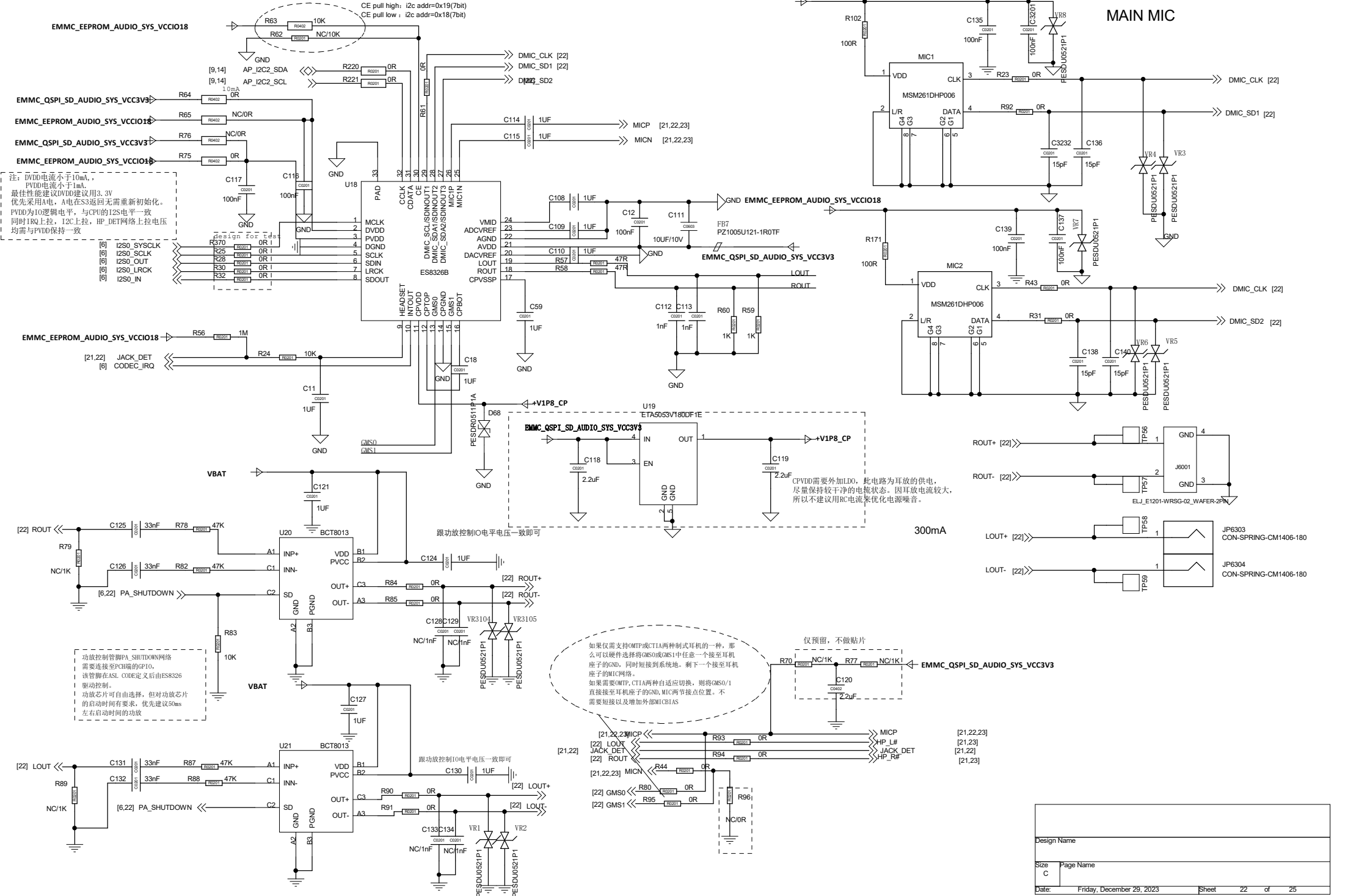
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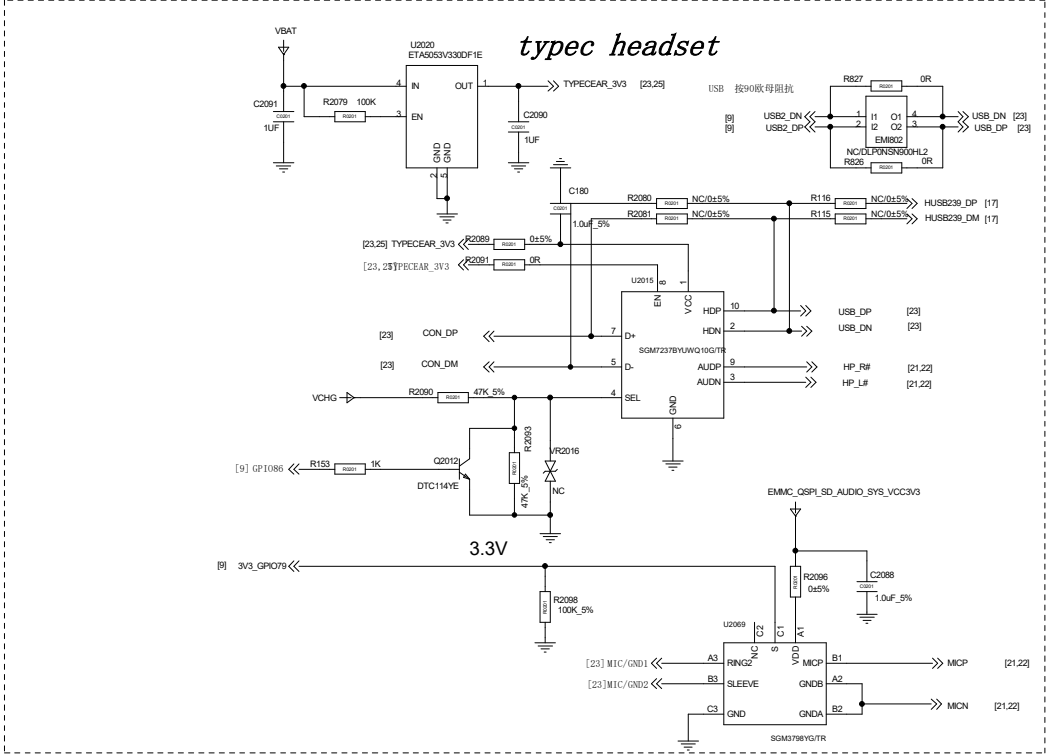
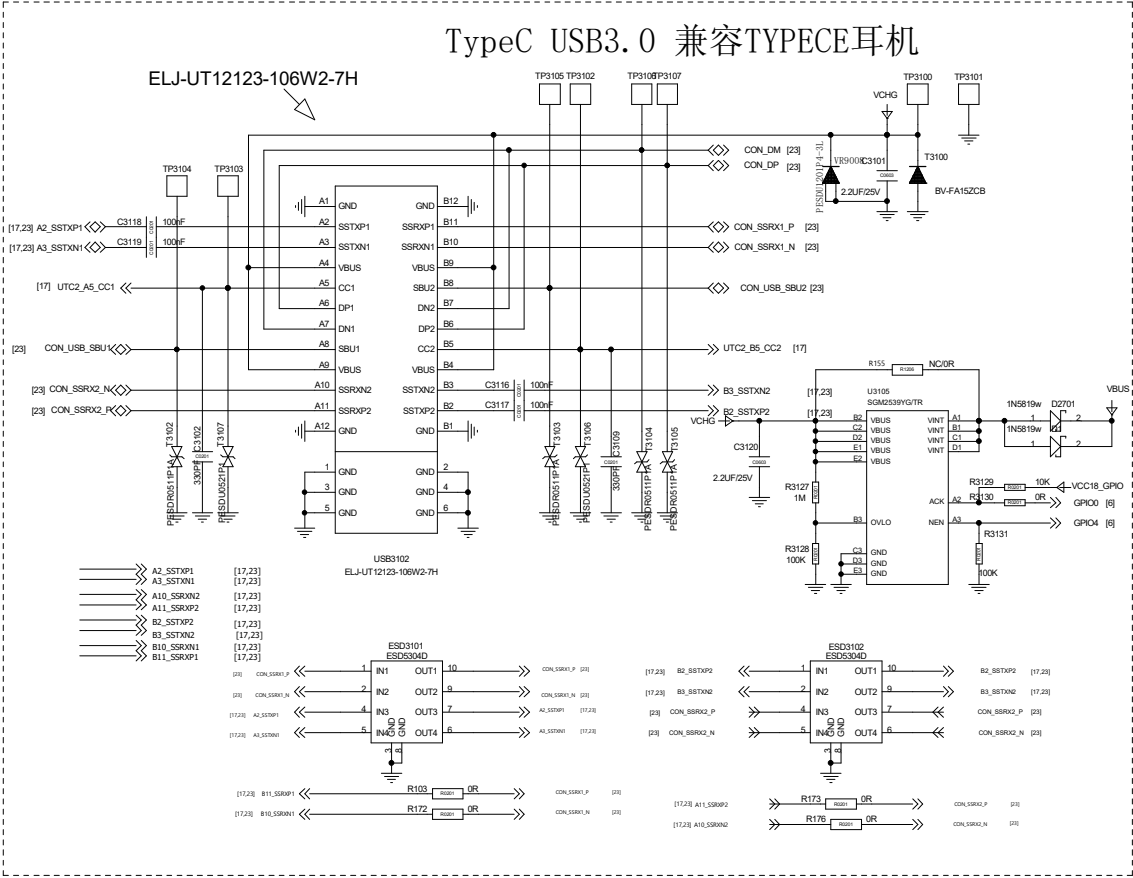
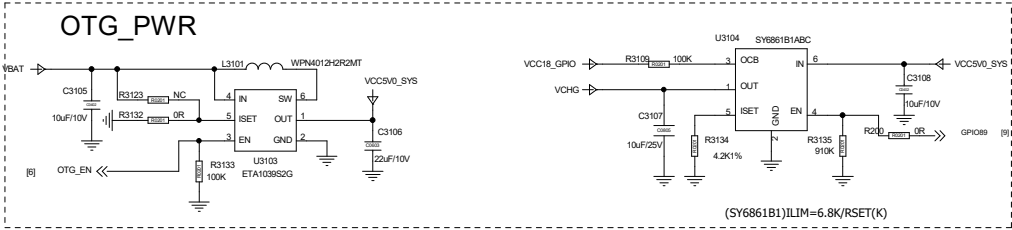
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		CHECKED:	DATED:		
QUALITY CONTROL:	DATED:				
RELEASED:	DATED:	SCALE:			SHEET: 21 25

Audio Codec





D

C

B

A

D

C

B

A

6

5

4

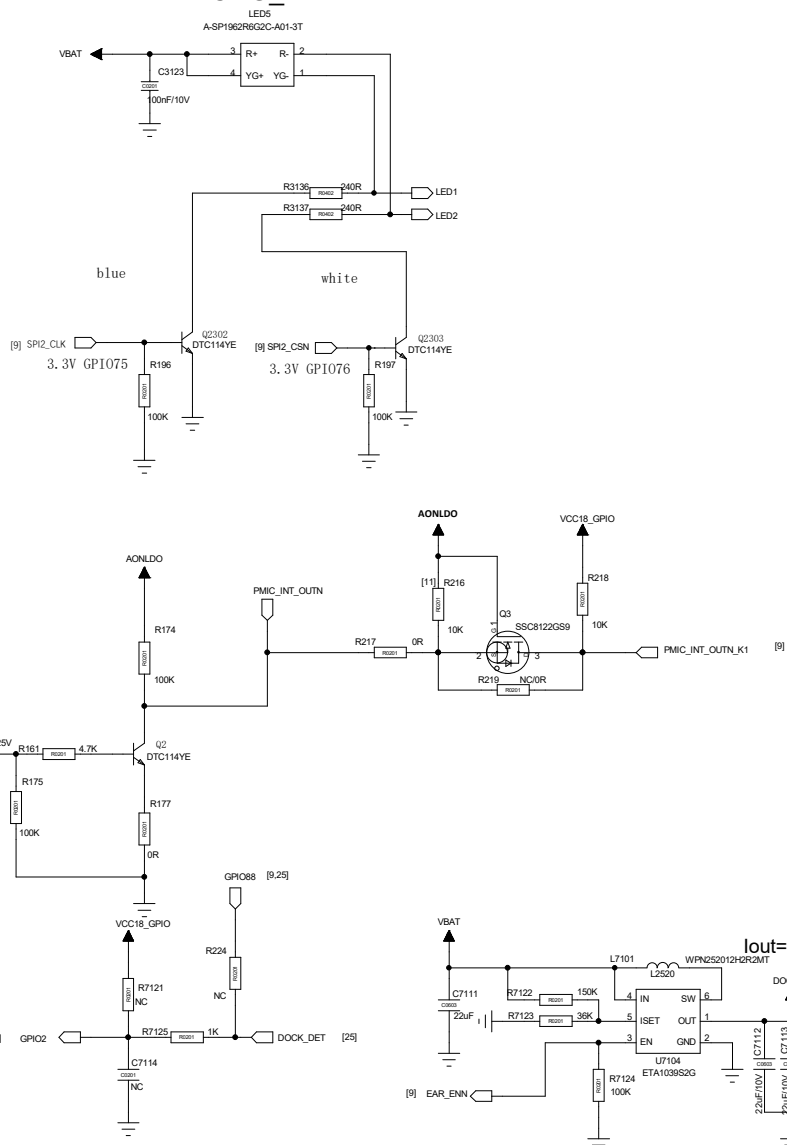
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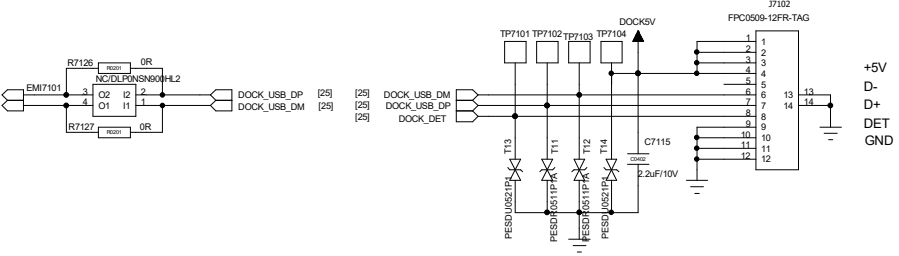
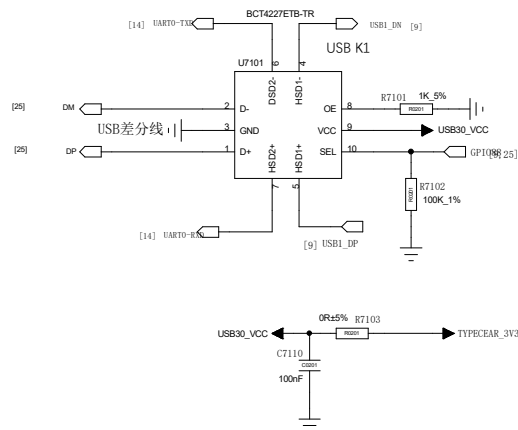
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CHG_LED



Iout=1.0A

OE	S	HSD1	HSD2
0	0	ON	OFF
0	1	OFF	ON
1	X	OFF	OFF



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CHECKED:		DATED:		TITLE:			
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